

Customer No.: 31561
Application No.: 10/604,173
Docket NO.: 10873-US-PA

REMARKS

Present Status of the Application

Claims 1-3 are rejected under 35 USC 102(b) as being anticipated by Kihara et al. (US-5,889,504)

After the following traversing of rejections, Claims 1-3 remain pending in the present application.

Discussion of claim rejections under 35 USC 102(b)

The Office Action rejected Claims 1-3 under 35 USC 102(b) as being anticipated by Kihara et al. (US-5,889,504, hereinafter "Kihara").

In regard to Claims 1-3, the element "redundant device" in Claims 1-3 in the present invention is referred to a plurality of TFTs or transistors located within a SINGLE shift register as illustrated in FIG. 4 of the present invention and in Paragraph [0021] of the present invention: " FIG. 4 schematically shows a circuit diagram of a circuit suitable for use in a driving stage having a redundant device of the present invention. Wherein, the redundant device comprises six transistors 442, 444, 446, 448, 450, and 452,..." On the other hand, the "redundant device" in Kihara is defined as a shift register that is complete, which is namely the right redundant shift register SR4 as found in FIG. 7 in Kihara. Therefore, the "redundant device" in Kihara is referred to a shift register; whereas, the "redundant device" in the present invention is referred to transistors instead. Therefore, it is clearly evident that the "redundant device" is NOT anticipated by Kihara.

In addition, as described in Kihara in Col. 10, lines 23-30, the normal shift registers

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and the redundant shift registers SR1-SR4 are each made of a plurality of clocked inverters 90, 91, and inverter 92, since it is described that SR2-SR4 are of "same structures as that of the left normal register SR1" which is also shown in FIG. 8 in Kihara. Whereas, the driving stages of the present invention utilizes "the redundant device comprises six transistors 442, 444, 446, 448, 450 and 452", which is found in Paragraph [0021] of the present invention. In summary, shift registers in Kihara uses inverters and the shift registers in the present invention uses redundant devices comprising of transistors

Based on the above traversing, Claims 1-3 are patentable over Kihara and should be allowed.

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CONCLUSION

For at least the foregoing reasons, it is believed that the pending Claims 1-3 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,


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